

**REMARKS**

Applicant appreciates the Examiner's thorough consideration provided the present application. Claims 1-21 are now present in the application. Claims 1, 5 and 19 have been amended. Claims 1, 5 and 19 are independent. Reconsideration of this application is respectfully requested.

**Allowable Subject Matter**

The Examiner has indicated that dependent claims 5-15 and 18 would be allowable if rewritten to include all of the limitations of the base claim and any intervening claims. Applicant appreciates the indication of allowable subject matter by the Examiner.

By the present amendment, claim 5 has been rewritten in independent form to include the subject matter of its base claim 1. Accordingly, it is believed that independent claim 5 and its dependent claims are in condition for allowance.

**Claim Rejections Under 35 U.S.C. § 102**

Claims 1-4, 16, 17 and 19-21 stand rejected under 35 U.S.C. § 102(b) as being anticipated by Okumura et al., U.S. Patent No. 6,331,844 (hereinafter "Okumura"). This rejection is respectfully traversed.

A complete discussion of the Examiner's rejection is set forth in the Office Action, and is not being repeated here.

Independent claim 1 recites "a voltage to current converting unit supplying current of a plurality of levels to a data line of the display panel according to logical combinations of the

sampled picture signal from the latch unit, using a current mirror method, wherein the voltage to current converting unit includes a plurality of poly-crystalline switching units formed on the display panel.”

Independent claim 19 recites “a plurality of pixel units located on the substrate” and “a data driving circuit located on the same substrate, the data driving circuit including a plurality of current paths, the data driving circuit supplying current of a plurality of levels to at least one of the plurality of pixel units by providing the current from at least one of the plurality of current paths, wherein the current mirror paths are formed on the substrate.”

Applicant respectfully submits that the combinations of elements set forth in claims 1 and 19 are not disclosed or suggested by the reference relied on by the Examiner.

In particular, Okumura nowhere discloses that the voltage-current converter includes a plurality of poly-crystalline switching units formed on the display panel, or that the data driving circuit including a plurality of current paths is located on the same substrate as the pixel units. In fact, all drawings in Okumura show that the voltage-current converter is *separated* from the TFT-LCD panel (see *e.g.*, FIGs. 1-3). In addition, the voltage-current converter disclosed in Okumura includes either a resistor-array (FIG. 5) or an OPAMP (FIG. 6), which make the voltage-current converter impracticable to be formed on the same substrate as the pixel units. This problem is addressed in paragraph [0022] of the specification of the present invention, and is solved by the present invention. Therefore, Okumura fails to teach “the voltage to current converting unit includes a plurality of poly-crystalline switching units *formed on the display panel*” as recited in amended claim 1 and “*a plurality of pixel units located on the substrate*” and “*a data driving circuit located on the same substrate, the data driving circuit including a plurality*

of current paths,... wherein *the current mirror paths are formed on the substrate*" as recited in claim 19.

Since Okumura fails to teach at least the above-noted features of independent claims 1 and 19, Applicant respectfully submits that claims 1 and 19 and their dependent claims (at least due to their dependency) are patentable over Okumura. Accordingly, reconsideration and withdrawal of the rejection under 35 U.S.C. § 102 are respectfully requested.

## **CONCLUSION**

All the stated grounds of rejection have been properly traversed and/or rendered moot. Applicant therefore respectfully requests that the Examiner reconsider all presently pending rejections and that they be withdrawn.

It is believed that a full and complete response has been made to the Office Action, and that as such, the Examiner is respectfully requested to send the application to Issue.

In the event there are any matters remaining in this application, the Examiner is invited to contact the undersigned at (703) 205-8000 in the Washington, D.C. area.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. §§1.16 or 1.17; particularly, extension of time fees.

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Respectfully submitted,

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